

Part of #29

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			Application Number	09/296,635	
			Filing Date	April 22, 1999	
			First Named Inventor	Ronald A. Welmer	
			Art Unit	2813	
Examiner Name	E. Kielin				
Sheet	1	of	1	Attorney Docket Number	M4065.0319/P319

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
OK	A	6,083,698	05/16/00	Tseng et al.	
OK	B	6,197,668	03/06/01	Gardner, et al.	
OK	C	US-2001/0020725	09/13/01	Okuno, et al.	

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>2</sup> -Number <sup>3</sup> -Kind Code <sup>4</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
OK	D	LU, et al., "Leakage Current Comparison Between Ultra-Thin Ta <sub>2</sub> O <sub>5</sub> films and conventional gate dielectrics" IEEE Electron Device Letters 19(9), 9/1998, pp. 341-342	
OK	E	ALERS, et al., "Intermixing at the Tantalum Oxide/Silicon Interface in Gate Dielectric Structures" Applied Physics Letters 73(11), 14 September 1998, pp. 1517-1519	
OK	F	LUAN, et al., "Ultra-thin High Quality Ta <sub>2</sub> O <sub>5</sub> Dielectric Prepared by In-Situ Rapid Thermal Processing" Electron Devices Meeting, held 6-9 December 1998, IEDM '98 Technical Digest, pp. 609-612.	

Examiner Signature	<i>E. Kielin</i>	Date Considered	8/5/02
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